

## QTHINK JOINS CADENCE VERIFICATION ALLIANCE PROGRAM

**San Diego, CA, June 25, 2007** - QuantumThink Group, Inc. (dba QThink), a global leader in the IC design industry, is pleased to announce its recent entry in the Verification Alliance Program from Cadence Design Systems. The program is a network of expert providers of services, training, and IP development that support Cadence® functional verification solutions and the Incisive® Plan-to-Closure methodology. Participants in this international program, are introduced to new verification technologies that dramatically improve the overall process and productivity of their design and verification teams.

Cadence has further highlighted QThink's design proficiency by designating the company as an Incisive Plan-to-Closure Methodology-Qualified Verification Alliance Member. Participants in this 22-member group have proven expertise in one or more of the methodology's four key elements. QThink's extensive experience in two of the key elements, verification planning and management, and assertion-based and formal verification, will allow them to partner with Cadence to meet the program's goal of extending verification resources and expertise for design teams facing verification-resource limitations.

"We are very excited to be a partner with Cadence in these unique programs," said Charlie Kahle, CTO and Vice President of Engineering at QThink. "We have a long-standing relationship with Cadence and believe the Cadence technology offers a great tool for use in providing verification services to our clients."

In partnering with Cadence, QThink can take advantage of a wide range of benefits. Once selected, members are given access to Cadence software solutions exclusively for developing specific IP or support. Partners are also provided training for new Cadence verification technologies and methodologies, and have access to phone support through Cadence's Customer Support Call Center. As an added bonus, Cadence works with QThink to promote their areas of expertise.

Cadence requires a rigorous certification process as a pre-requisite to becoming a member of their Verification Alliance and Incisive Plan-to-Closure Methodology-Qualified Verification Alliance Programs. With QThink's prior expertise using Hardware Verification Languages such as System Verilog and *e*, along with their experience using assertion-based verification and formal verification techniques, QThink was able to quickly pass this certification process.

"We are delighted to count QThink as a new and already very active member of the Cadence Verification Alliance Program," says Susan Peterson, Verification Alliance Program Manager at Cadence. "I am sure that our customers will find QThink's combined experience in planning, management and formal verification to be very useful in the implementation of new verification methodology and technology."

For more information on QThink's membership in the Verification Alliance or Incisive Plan-to-Closure Methodology-Qualified Verification Alliance Programs, please visit QThink's Cadence Verification Alliance member page at

<http://www.cadence.com/partners/verificationalliance/member.aspx?member=QThink> or the

Plan-to-Closure Methodology-Qualified Verification Alliance Member page at [http://www.cadence.com/partners/verificationalliance/ipcm\\_members.aspx](http://www.cadence.com/partners/verificationalliance/ipcm_members.aspx).