

QThink® Front-end Design Services has been instrumental in addressing the fabless semiconductor's ever growing need for SoC design and verification expertise

QThink's strategic geographic positioning in areas of high-interest provides unparalleled quality, support and services to QThink's SoC clients

San Diego, CA, July 31, 2008 – QThink today announced the company's expansion of its design services in the fastest growing area of SoC design and verification a success. Throughout the Companies nearly 10 years of existence, the design and verification services have effectively complemented QThink's robust physical implementation, offering a one-stop-shop for today's complex SoC designs. The availability of massive amounts of logic resources in 65nm process technologies and below, together with the high level of complex IP integration has resulted in SoC design verification to consume the significant portion of design resources and time. In addition many SoC companies face limited engineering expertise with in-depth knowledge of advanced verification languages and techniques, resulting in potential schedule impacts as well as increased risk in releasing the design to the manufacturing process.

"There are three sturdy pillars of success upon which QThink front-end design services thrive - the vast pool of engineering skill-set in advanced verification we bring to each project, our tactical geo positioning with regard to the best interest of our clients, and our deliberate focus from Spec to GDSII," said Chief Technology Officer and Vice President of Design Services, Charlie Kahle. "We are proud to offer in-depth advanced verification design expertise which includes, but is not limited to: C++, SystemC modeling and exploration, RTL modeling, TLM system design and analysis, SystemVerilog, Vera, Specman, OVM/RVM/VMM, plus expertise on the simulators from the major EDA suppliers such as Cadence, Mentor and Synopsys."

QThink has made significant investment in its establishment of strategic locations in Silicon Valley, San Diego, CA, and Bangalore, India to accommodate its growing client base. Front-end design services require the close proximity of engineers, a fact QThink has accordingly acted upon. Its positioning rules out design services companies which focus the majority of their design resources outside of these locations. QThink's front-end design services blend well with smaller companies' inability to afford a timely ramp to the latest design verification methods in comparison to larger companies with limited engineering resources to complete the number of SoCs planned for simultaneous execution.

QThink's dedicated focus from Spec to GDSII complements large fabless companies which manage their own manufacturing infrastructure. QThink has a

large customer base ranging from tier-1 wireless, networking, storage and consumer players to early stage startups. Strong participation in 65nm and 45nm SoC designs with special focus on high performance as well as low-power techniques have also been key focus areas for the company.

QThink's robust and optimized methodology coupled with an extensive team of design experts continues to demonstrate on-time and right-by-construction project completion. QThink ensures its utilization of the latest design automation platforms from the leading EDA suppliers including Cadence, Magma, Mentor and Synopsys.

For more information regarding QThink's design services capabilities, please contact QThink or visit www.qthink.com.

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About QThink®

QThink® is a world leader in providing ASIC and SoC design services to leading-edge semiconductor companies world wide. **QThink** offers flexible technical and business engagement models with customer design interfaces covering architecture and specification to logic design and verification to physical implementation. **QThink's** robust and optimized methodology, coupled with an extensive team of design experts, has continuously demonstrated on-time and right-by-construction silicon tapeouts. **QThink's** vast pool of engineering experts are engaged with complex designs from 130nm to 45nm, targeting broad market requirements from low-power handheld to high performance networking and storage devices. **QThink** has successfully delivered over 200 designs to date. **QThink** has strong partnership and collaboration with EDA vendors embracing the state of the art design automation tools.